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200310791-1

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Attorney Docket Number

Sheet 1

Examiner

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Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY				Pages, Columns, Lines, Wher Relevant Passages or Relevan Figures Appear	
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
NL	1	FOLTIN, M., et al., "Efficient Stimulus Independent Timing Abstraction Model Based on a New Concept of Circuit Block Transparency", DAC 2000, June 10-14, 2002, p. 1-6	:
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NL	3	HOSEGRAHAR, I., et al., "A Report of Static Timing Analysis", VLSI Design Automation (EE7V81), Summer 2001, p. 1-21	

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